

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

11002 U.S. PTO
09/838057
04/19/01



In re application of: William J. Armstrong, et al. : Date: April 19, 2001
Group Art Unit: Unassigned : IBM Corporation
Examiner: Unassigned : Intellectual Property Law
Serial No.: Unassigned : Dept. 917, Bldg. 006-1
Filed: Herewith : 3605 Highway 52 North
Title: METHOD AND APPARATUS FOR
ALLOCATING PROCESSOR
RESOURCES IN A LOGICALLY
PARTITIONED COMPUTER SYSTEM

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

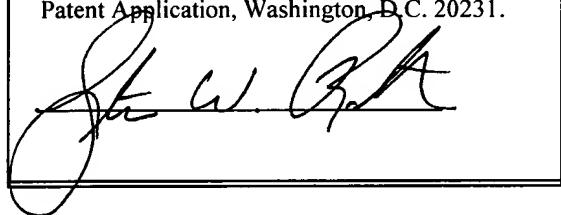
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. §1.97. In accordance with 37 C.F.R. §1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standard of materiality defined in 37 C.F.R. §1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. §1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner. In accordance with 37 C.F.R. §1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

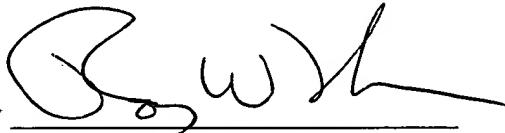
Serial No. (Unassigned)
Docket No. ROC9-2000-0252-US1

This Information Disclosure Statement is being filed under 37 C.F.R. §1.97(b)(1) within three months of the filing date of the present application. Accordingly no filing fee is required.

EXPRESS MAIL CERTIFICATE
Express Mail No. EK899953262US
Date: April 19, 2001
I certify that I am depositing the enclosed or attached paper with the U.S. Postal Service "Express Mail Post Office to Addressee" service on the above date, addressed to the Assistant Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.


Respectfully submitted,

WILLIAM J. ARMSTRONG, et al.

By 
Roy W. Truelson

Attorney Reg. No. 34,265
Telephone: 1-507-289-6256

Serial No. (Unassigned)
Docket No. ROC9-2000-0252-US1

FORM PTO-1449 (Modified)

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION
DISCLOSURE STATEMENT

(Use several sheets if necessary)

Page 1 of 1

ATTY. DOCKET NO.
ROC9-2000-0252-US1

SERIAL NO. (Unassigned)

APPLICANT: William J. Armstrong, et al.

FILING DATE: (Herewith)

GROUP: (Unassigned)

11002 U.S. PT 09/838057
04/19/01

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER								DATE	NAME	CLASS	SUB-CLASS	FILING DATE (IF APPRO.)
AA	5	6	5	9	7	8	6		8/19/1997	George, et al.	395	653	
AB	4	8	4	3	5	4	1		6/27/1989	Bean, et al.	364	200	
AC													
AD													
AE													
AF													
AG													
AH													
AI													
AJ													
AK													

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
														YES	NO
	AL														
	AM														
	AN														
	AO														
	AP														

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

AR	U.S. Patent Application Serial No. 09/672,043, entitled "Technique for Configuring Processors in System With Logical Partitions", filed September 29, 2000.													
AS	IBM Corporation, S/390 Processor Resource/Systems Manager Planning Guide (IBM Pub. No. GA22-7236-04, 5 th Edition, March, 1999)													
AT														

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.